

Web

Results 1 - 10 of about 475,000 for **programmable burst length** (0.48 seconds)

Semiconductor | Graphics Memory | DDR SDRAM

Range of operating frequencies, **programmable burst length** and **programmable latencies** allow the device to be useful for a variety of high performance memory ...
[www.samsung.com/Products/Semiconductor/](http://www.samsung.com/Products/Semiconductor/GraphicsMemory/DDRSDRAM/128Mbit/K4D263238E/K4D263238E.htm)
GraphicsMemory/DDRSDRAM/128Mbit/K4D263238E/K4D263238E.htm - 47k -
[Cached](#) - [Similar pages](#)

DRAM | Hynix Semiconductor

Programmable CAS latency 3 supported; **BURST LENGTH - Programmable burst length**
2 / 4 / 8 with both sequential and interleave mode; **AUTO PRECHARGE** ...
[www.hynix.com/datasheet/eng/dram/details/dram_17_HY5MS5B6LF\(P\).jsp](http://www.hynix.com/datasheet/eng/dram/details/dram_17_HY5MS5B6LF(P).jsp) - 53k -
[Cached](#) - [Similar pages](#)

logiCVC

... interface speed; **Programmable** system data interface speed (only for DDR SDRAM);
Programmable burst length; **Burst length** applies for both read and write ...
www.logicbricks.com/english/products/product_lines/product_line_3.html - 19k -
[Cached](#) - [Similar pages](#)

A-DATA

• **Programmable Burst length** (2,4,8) • Serial Presence Detect with EEPROM • Module bank :one physical bank • Height 31.75mm, six layers ...
www.adata.com.tw/en/products-d-un-ddr400.htm - 15k - [Cached](#) - [Similar pages](#)

[PPT] PLX Technology

File Format: Microsoft Powerpoint 97 - [View as HTML](#)
4LWord bursts, **programmable burst length**, & unlimited **burst**. Unaligned byte transfers.
Q202. 23. PCI 9056. PCI Bus Mastering I/O Accelerator ...
www.jungo.com/download/windriver_seminar_plx.ppt - [Similar pages](#)

BroNet - Wireless & Broadband

Programmable Burst length (2, 4, 8). **Programmable Burst** type (Sequential & Interleave). Edge aligned data output, center aligned data input. ...
www.bronet.com.au/Index.asp?prmt=7112CATA&Loc=111HOME - 100k -
[Cached](#) - [Similar pages](#)

[PDF] UNITED STATES OF AMERICA BEFORE FEDERAL TRADE COMMISSION PUBLIC ...

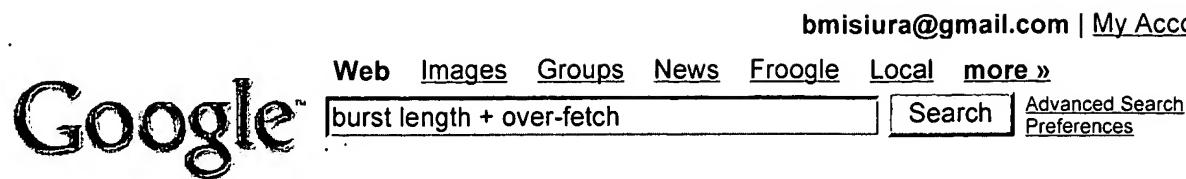
File Format: PDF/Adobe Acrobat - [View as HTML](#)
\$100000 more than **programmable burst length** in design costs (Geilhufe, Tr. 9594:5-12);
... reverted to **programmable burst length** (Soderman, Tr. 9369:12-23), ...
www.ftc.gov/os/adjpro/d9302/030724ccrebuttalnotice.pdf - [Similar pages](#)

about

Read and write accesses to the DDR SDRAM are **burst** oriented, with the **burst length** being **programmable**. The **burst length** determines the maximum number of ...
www.geil.com.tw/portal/about.php - 18k - [Cached](#) - [Similar pages](#)

IP.com's Prior Art Database

The disclosed method enables **programmable burst lengths**, giving additional flexibility



Web

Results 1 - 10 of about 52 for **burst length + over-fetch**. (0.87 seconds)

Beyond3D Forum - Adding full random memory of a CPU

expense of the **burst length** and Generally larger cost when you have a miss ... Neither has

any optimisation for **overfetch** other than assuming that a ...

www.beyond3d.com/forum/showthread.php?t=16152 - 148k - Supplemental Result -

[Cached](#) - [Similar pages](#)

Method for improving cache-miss performance patent

For example, if a **burst length** is four and a memory read operation is performed, ... It should be noted that SDRAM **burst** reads may **over-fetch** data; ...

www.freshpatents.com/Method-for-improving-cache-miss-performance-dt20041202ptan20040239680.php - 97k - [Cached](#) - [Similar pages](#)

[PDF] PowerPC 6xx/7xx Inverse Assembler

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Record **length**, in bytes, not including the Record type field (2 characters) ... **Overfetch**

refers to instructions which are fetched but not executed by the ...

www.cs.agilent.com/manuals/ala20/PPC6xx7xx.pdf - Supplemental Result - [Similar pages](#)

The Young Writer's Club - Member's Stories No:7967

... her knee-length hair, the hair she had grown since the age of seven. ... "My mouth is hot, my face is hot, I'm hot all over. Fetch me some wine, Karaea. ...

www.cs.bilkent.edu.tr/~david/derya/storys6/story7967.htm - 32k - [Cached](#) - [Similar pages](#)

The Willow Queen

... She refused to keep her knee-length hair, The hair She had grown since The age of seven. ... Arae, keeping an eye on Shokun-yiban, **burst** into tears. ...

coldandugly.net/marajade/writings/willow_queen.htm - 38k - Supplemental Result -

[Cached](#) - [Similar pages](#)

AskTom "Problem with BULK COLLECT with million r..."

See above -- You would "over **FETCH**" technically -- We can skip all of the ... I also needs to COMMIT every 100,00 records Or the rbs will **burst** in flames. ...

asktom.oracle.com/pls/ask/f?p=4950:8:9220374210870174093::NO::LAST_PAGE:8:YES - 238k - Supplemental Result - [Cached](#) - [Similar pages](#)

Complete Mages' Guild Magic Guide

... damage to any target; flammable targets will **burst** into bright ... to remain there for a set **length** of time ... **Fetch** has no significant tugging force and cannot pull ...

qfgc.com/guild/Mage.html - 101k - Supplemental Result - [Cached](#) - [Similar pages](#)

Crossroads' Edge

Suddenly, a pack of ravening orcs **burst** forth. eyes burning with hate, ... he raised The curved Mordor-sword in both hands, its entire **length** dripping with ...

www.cr-dreams.com/faramirfic.htm - 110k - Supplemental Result - [Cached](#) - [Similar pages](#)

[PDF] FACTORING FILE ACCESS PATTERNS AND USER BEHAVIOR INTO CACHING ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

the variable **length** data for different access requests. ... **burst** load is placed on the network as prefetching is done when bandwidth is. available. ...